

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	3707	257/678	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/01/07 08:17
L2	3707	257/678	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/01/07 08:18
L3	6	1 and (integrat\$3 adj circuit adj chip\$2) with batter\$3 with package	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/01/07 08:21
L4	113	257/683	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/01/07 08:21
L5	1	4 and (integrat\$3 adj circuit adj chip\$2) with batter\$3 with package	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/01/07 08:21
L6	1162	257/684	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/01/07 08:26
L7	2	6 and (integrat\$3 adj circuit adj chip\$2) with batter\$3 with package	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/01/07 08:27
L8	1236	257/685	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/01/07 08:29
L9	34	8 and batter\$3	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/01/07 08:27
L10	2	9 and (integrat\$3 adj circuit adj chip\$2) with batter\$3	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/01/07 08:30

L11	2926	257/686	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/01/07 08:31
L12	3	11 and (integrat\$3 adj circuit adj chip\$2) with batter\$3	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/01/07 08:31
L13	2807	257/690	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/01/07 08:31
L14	1	13 and (integrat\$3 adj circuit adj chip\$2) with batter\$3	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/01/07 08:50
L15	3838	174/52.4	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/01/07 08:32
L16	118	15 and batter\$3	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/01/07 08:50
L17	756	361/717	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/01/07 08:51
L18	2	17 and (integrat\$3 adj circuit adj chip\$2) with batter\$3	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/01/07 08:52
L19	875	361/718	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/01/07 08:52
L20	1	19 and (integrat\$3 adj circuit adj chip\$2) with batter\$3	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/01/07 08:52

L21	1994	361/719	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/01/07 08:53
L22	1	21 and (integrat\$3 adj circuit adj chip\$2) with batter\$3	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/01/07 08:53
L23	252	361/743	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/01/07 08:54
L24	1	23 and (integrat\$3 adj circuit adj chip\$2) with batter\$3	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/01/07 08:54
L25	821	361/779	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/01/07 08:55
L26	1	25 and (integrat\$3 adj circuit adj chip\$2) with batter\$3	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/01/07 08:55
L27	862	361/735	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/01/07 08:55
L28	0	27 and (integrat\$3 adj circuit adj chip\$2) with batter\$3	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/01/07 08:55